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PTO/SB/21 (12/97)
Approved for use through 9/30/2000. OMB 0651-0031
Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE



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| TRANSMITTAL FORM <i>(to be used for all correspondence after initial filing)</i> | Application Number | 10/688,023 | |
| | Filing Date | October-16, 2003 | |
| | First Named Inventor | Geeta Desai | |
| | Group Art Unit | 2818 | |
| | Examiner Name | | |
| Total number of pages in this submission | 7 | Attorney Docket Number | 5201-27700 / 03-1349 |

ENCLOSURES (check all that apply)

| | | |
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| <input type="checkbox"/> Fee Attached | <input type="checkbox"/> Drawing(s) | <input type="checkbox"/> Appeal Communication to Board of Appeals and |
| <input type="checkbox"/> Amendment/Response | <input type="checkbox"/> Licensing-related Paper | <input type="checkbox"/> Appeal Communication to Group (Appeal Notice, Brief, Reply Brief) |
| <input type="checkbox"/> After Final | <input type="checkbox"/> Petition Routing Slip (PTO/SB/69) and Accompanying Petition | <input type="checkbox"/> Proprietary Information |
| <input type="checkbox"/> Affidavits(s)/declaration(s) | <input type="checkbox"/> To Convert a Provisional Application | <input type="checkbox"/> Status Letter |
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| <input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53 | | |

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

| | |
|-----------------|---|
| Individual Name | Leo Peters, Reg. No. 33,562, Phone: [+1] 408-433-7191 |
| Signature | |
| Date | 02/11/04 |

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| I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope to: Assistant Commissioner for Patents, Washington, D.C. 20231 on this date: 02/11/04 | |
| Typed or printed name | Connie Del Castillo, Phone: [+1] 408-433-7191 |
| Signature | Connie del Castillo |
| Date | 02/11/04 |

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of :

Geeta Desai et. al.

Serial No. : 10/688,023

Filed : October 16, 2003

For : Integrated NAND And Nor-Type
Flash Memory Device And Method
Of
Using The Same

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Group Art Unit : 2818

Examiner :

Atty Docket : 5201-27700 / 03-
1349

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Connie Del Castillo

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INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P. O. Box 1450
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Dear Sir:

The references listed in the attached form, copies of which are attached, may be material to examination of above-identified application. Applicants submit these references in compliance with their duty of disclosure pursuant to 37 CFR 1.56 and 1.97.

It is requested that the information disclosed herein be made of record in the application.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

If it is determined that any additional fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 12-2252.

LSI Logic Corporation
1551 McCarthy Blvd., MS D-106
Milipitas, CA 95035
408-433-7475

Date: 02/11/04

Respectfully submitted,



Leo Peters

Reg. No. 33,562



FEB 17 2004

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PTO/SB/08A (10-96)

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Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT****Complete if Known**

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| Application Number | 10/688,023 |
| Filing Date | October-16, 2003 |
| First Named Inventor | Geeta Desai |
| Group Art Unit | 2818 |
| Examiner Name | |
| Attorney Docket No. | 5201-27700 / 03-1349 |

Sheet 1 of 1

U.S. PATENT DOCUMENTS

| Examiner Initials | Cite No. | U.S. Patent Document | | Name of Patentee or Applicant of Cited Document | Date of Publication of Cited Document MM-DD-YYYY | Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear |
|----------------------|-------------|----------------------|-------------------------|--|--|---|
| | | Number | Kind Code (if known) | | | |
| | | 6,197,639 | | Lee et al. | 03-06-2001 | |
| | | 6,570,213 | | Wu | 05-27-2003 | |
| | | 6,549,461 | | Park et al. | 04-15-2003 | |
| | | 6,370,062 | | Choi | 04-09-2002 | |

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

| Examiner Initials | Cite No. | Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, caalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where | T |
|----------------------|-------------|---|---|
| | | "Flash Decision" - Author(s) - John Edwards | |
| | | NAND Flash Applications Design Guide - Author(s) - Toshiba America Electronic Components, Inc. | |

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